10/588636

JAP20 Rec'd PCT/PTO 04 AUG 2006

APPLICATION DATA SHEET

Application Information

Subject Matter:: Utility
Application Type:: Regular

Title:: Semiconductor Device Test Apparatus and

Method

Attorney Docket Number:: NAA243

Request for Non-Publication?::

Suggested Drawing Figure::

1

Total Drawing Sheets::

10

Applicant Information

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Given Name:: Kazuyoshi
Family Name:: Okawa

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Country of Residence:: Japan

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Country of Mailing Address:: Japan
Postal or Zip Code of Mailing Address:: 179-0071

Applicant Authority Type:: Inventor Primary Citizenship Country:: Japan Given Name:: Junko Family Name:: Ogino

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Country of Residence:: Japan

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City of Mailing Address:: Tokyo
Country of Mailing Address:: Japan
Postal or Zip Code of Mailing Address:: 179-0071

Applicant Authority Type:: Inventor
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Given Name:: Masayuki

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Applicant Authority Type::

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Primary Citizenship Country::

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Given Name::

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Family Name::

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City of Mailing Address:: Country of Mailing Address::

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Postal or Zip Code of Mailing Address:: 179-0071

Correspondence Information

Correspondence Customer Number::

25271

Electronic Mail::

office@patentz.com

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/JP2004/001805	02/18/04

Assignee Information

Assignee Name::

Advantest Corporation

Street of Mailing Address::

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City of Mailing Address::

Tokyo

State or Province of Mailing Address::

Japan

Postal or ZIP Code of Mailing Address:: 179-0071